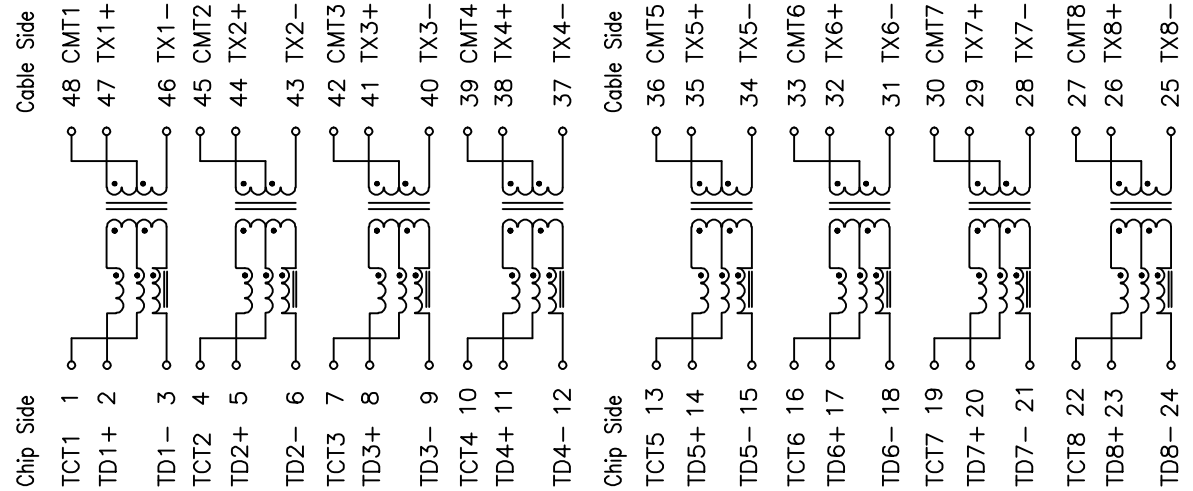


SUGGESTED PCB LAYOUT

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE ±0.25mm



ELECTRICAL CHARACTERISTICS @25°C :

- INSULATION HI-POT(PRI./SEC.) : 1500Vrms, 1mA, 60SEC
- TURN RATIO (±5%) : TX = 1CT : 1CT , RX = 1CT : 1CT
- OCL : 350uH MIN. @100kHz, 0.1V WITH 8mA DC BIAS  
200uH MIN. @100kHz, 0.1V WITH 18mA DC BIAS
- LEAKAGE INDUCTANCE : 0.5uH MAX @100kHz, 0.1V
- DCR : 1.2Ω MAX.
- INSERTION LOSS (dB MAX.) : -1.1dB MAX.(@1-100MHz)
- RETURN LOSS (dB MIN.) : 1-30MHz 40MHz 50MHz 60-80MHz 100MHz  
-18 -14.4 -13.1 -10 -8
- CROSS TALK (dB MIN.) : 1-100MHz : -30dB MIN.
- CMRR (dB MIN.) : 1-100MHz : -30dB MIN.
- OPERATING TEMPERATURE RANGE : 0°C~+70°C
- IEEE802.3at COMPLIANT PERFORMANCE----720mA Current capability
- RoHS COMPLIANT

							TITLE 10/100/1000 BASE-T PoE+ TRANSFORMER	
							DWG. NO. ATPL-510PR	
RELEASE			10/03/2018	SHIRLEY	BETTY	BETTY	UNITS: M/M	SAFETY
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	SHEET 1 of 1
REVISIONS						10/03/18		DRAW SHIRLEY